<u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes

Examiner: Richard Booth

Serial No.:

09/945512

Group Art Unit: 2812

Filed:

August 30, 2001

Docket: 1303.027US1

Title:

IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL

BARRIER INTERPOLY INSULATORS

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. 09/945507	Filing Date August 30, 2001	Attorney Docket 1303.014US1	Title FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/945395	August 30, 2001	1303.019US1	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/943134	August 30, 2001	1303.020US1	PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH ASYMMETRICAL TUNNEL BARRIERS
09/945498	August 30, 2001	1303.024US1	INTEGRATED CIRCUIT MEMORY DEVICE AND METHOD
09/945554	August 30, 2001	1303.028US1	SRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS

COMMUNICATION CÖNCERNING RELATED APPLICATIONS

Serial Number: 09/945512

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Title: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Page 2 Dk: 1303.027US1

09/945500	August 30,	1303.029US1	PROGRAMMABLE MEMORY		
-	2001		ADDRESS AND DECODE CIRCUITS		
			WITH LOW TUNNEL BARRIER		
			INTERPOLY INSULATORS		
10/028001	December	1303.035US1	PROGRAMMABLE ARRAY LOGIC OR		
	20, 2001		MEMORY WITH P-CHANNEL		
			DEVICES AND ASYMMETRICAL		
			TUNNEL BARRIERS		
10/081818	February	1303.045US1	ATOMIC LAYER DEPOSITION OF		
	20, 2002	•	METAL OXIDE AND/OR LOW		
			ASYMMETRICAL TUNNEL BARRIER		
	·		INTERPLOY INSULATORS		
10/177096	June 21,	1303.063US1	GRADED COMPOSITION METAL		
	2002		OXIDE TUNNEL BARRIER		
			INTERPOLY INSULATORS		
Respectfully submitted,					
LEONARD FORBES					

By Applicant's Representatives,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11th day of February, 2004.

Signature